

# Venu Gopal Reddy Allipuram

98 S Duncan Avenue, Apt # 5,  
Fayetteville, AR 72701

email: [vallipu@uark.edu](mailto:vallipu@uark.edu)

Ph: 479-871-3397

<http://www.pixelheart.com>

---

## OBJECTIVE

Seeking a challenging and exciting career in the area of analog, mixed signal circuit design and modeling.

## EDUCATION

- **Master of Science in Electrical Engineering** (since Aug 2002) **GPA: 3.88/4.0**  
**Expected graduation:** December 2004  
University of Arkansas, Fayetteville, AR, USA  
Completed all the course requirements.  
Thesis: Modeling and design of Sigma Delta ADC for sensor systems.
- **B.Tech in Electronics and Communications Engineering** (1997-2001) **GPA: 4.0/4.0**  
Jawaharlal Technological University (J.N.T.U), Hyderabad, India

## EXPERIENCE

---

### GRADUATE RESEARCH EXPERIENCE

---

**Graduate Research Assistant at Mixed Signal Computer Aided Design Lab, University of Arkansas.**  
(June, 2002–Present)

- Currently working on the design of Sigma Delta ADCs for an integrated sensor system.
  - Modeling and design of the analog front end for the same system at the behavioral level using Cadence IC tools.
  - Working on board level design and testing of analog part of the sensor system.
  - Designed a high swing folded cascode OP-AMP in 0.35 $\mu$ m MOI5 process.
  - Designed and developed the MSCAD lab website. The website includes online search, upload/download, secured internal web and message groups.
- 

### TEACHING EXPERIENCE

---

- **Part-time On-campus Tutor at University of Arkansas** (June, 2002–Dec, 2003)  
Worked as a tutor at Student Support Services, University of Arkansas. The responsibilities include tutoring undergraduate students all levels of Mathematics along with Sociology Statistics, Physics and Chemistry.
- 

### UNDERGRADUATE INDUSTRIAL EXPERIENCE

---

- **Part-time DSP Engineer at GI Systems, Hyderabad, India** (April, 2001–April, 2002)  
Designed software audio filters for medical transcription devices using the DSP Algorithms.
  - **Undergraduate Research Experience, at Defense Research Laboratory, India.** (Oct, 2000 – April, 2001)  
Involved in the design of FPGA Frame Synchronizer chip for a missile communication block.
- 

### FREELANCE EXPERIENCE

---

**Freelance Web developer, User Interface Designer, Graphic Designer, Photographer**  
(Jan 1997–Dec 2003)

- **Webindian Technologies:** Designed and developed more than 50 websites (listed in my homepage <http://www.pixelheart.com>) for Webindian Technologies. (<http://www.webindian.com>).

- **Compulse Systems Pvt. Limited, Hyderabad, India:** Designed and developed an online user front end for the network application named VCM (Virtual Cluster Monitor) using Java Server pages, Java Script, HTML and Adobe and Macromedia Tools.

## PROJECTS

- **Analog IC Design Projects:** Currently working on Sigma Delta data converter ADC design for sensor systems. Also designed and tested a high swing cascode OP-Amp in 0.35micron MOI5.
- **Digital IC Design Projects:** Designed, laid out and tested a 32 bit DNA string comparison chip. Designed and laid out a pseudorandom bit generator for BIST architectures.
- **Mixed Signal Modeling and Simulation Projects:** Designed and implemented a FM modulator and demodulator system in VHDL, VHDL AMS and simulated in Mentor System Vision.
  - > Currently working on the modeling of Sigma Delta data converter in VHDL-AMS.
  - > Developed a VHDL based digital circuit fault model generation, and pattern generation tools in C++ language as project for the course Digital Circuit Testing.
- **Undergraduate Projects:** Design of a PCM code synchronizer FPGA testing chip for a missile communication block. The tools used were ALTERA Max Plus 2 and Model Sim.

## TECHNICAL SKILLS

<b>Simulation &amp; Layout Tools</b>	Mentor Graphics IC Tools, Saber, Cadence Virtuoso, Mentor IC Station, Model Sim, Cadence Orcad Capture, PSPICE, Mentor System Vision (VHDL & VHDL AMS), MATLAB. Skills include simulation tools setup and maintenance.
<b>HDL</b>	VHDL, VHDL -AMS, Verilog, MAST.
<b>Programming Languages</b>	C, C++, Knowledge in Java, FORTRAN, Perl for Web
<b>Scripting Languages</b>	HTML, VRML, Java Script, Jscript, Action script ASP, JSP, and PHP.
<b>Multimedia Tools</b>	Adobe Photoshop, Illustrator, Macromedia Flash, Dream weaver, Cold Fusion, Fireworks, Freehand, Fontographer, 3D Studio
<b>Software Tools</b>	Microsoft Word, Excel, PowerPoint, Outlook, FrontPage, Visual Studio, Macromedia Dreamweaver.
<b>Operating Systems</b>	Sun UNIX, Windows Server, Linux, skills include the networking and administration.

## COURSE WORK

- **IC Design and Fabrication Courses:** Introduction to I.C Design, I.C Design lab, Advanced IC Design, Analog Integrated Circuits, Integrated Circuit Fabrication Technology, Semiconductor Devices, VLSI Technology, Practical Digital Integrated Circuits.
- **Communication Related Courses:** Analog & Digital Communications, Optical and Wireless Communications, Digital Signal Processing, Microwave Devices and Circuits.
- **Computer Related Courses:** Artificial Intelligence, Networking, C, and C++ Data Structures.
- **Management Courses:** Industrial Management Principles and Applications.
- **I.C. Testing Courses:** Digital Circuit Testing.

## MEMBERSHIP

- IEEE (Institute of Electrical and Electronics Engineers) member since 2002
- IMAPS (International Microelectronics and Packaging Society) member since 2002.

## ACHIEVEMENTS

- Certificate of merit for an outstanding performance in undergraduate study.
- Merit scholarship for being in the top 5% in the State Board Intermediate Examination.
- Got the best graphic designer of the month by India Times, an online news portal.

**REFERENCES:** Available upon request.