

# TIEJUN CAO

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## **OBJECTIVE**

- Seeking a challenging position in analog and mixed-signal circuit design area.

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## **EDUCATION**

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**August 2000 to present      University of Arkansas      Fayetteville, Arkansas**

- M.S. student in Electrical Engineering
- Focused on analog and mixed-signal circuit design.
- GPA 3.8.

**Relevant Courses:**

Semiconductor Devices  
Analog Integrated Circuits  
Modeling and Simulation of Mixed Signal Circuit  
Integrated Circuit Design (Introduction, Lab I & Lab II)  
Mixed Signal Testing  
Asic Design  
Minicomputer Applications  
Telecommunication

**Sept. 1989 to June 1993      Wuhan University      Wuhan, China**

**Bachelor of Science in Electrical Engineering**

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## **EXPERIENCE**

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**August 2000  
to present      University of Arkansas      Fayetteville, Arkansas**  
**Dept. of Electrical Engineering**  
**H. Alan Mantooth, Ph.D., P.E.**

**GRADUATE RESEARCH ASSISTANT**

- Working on project “Rad-Hard High-Voltage Bias Circuitry for a MEMS Microgyroscope for Micronavigation Systems” sponsored a NASA research award (NRA-99-OSS-05-1347) to Nanopower.

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## **COMPUTER SKILLS**

- IC design tools: Mentor Graphics & Cadence IC design tools
- Simulation tools: Pspice, Saber, Matlab
- Programming languages: FORTRAN
- Hardware description languages: VHDL-AMS
- Operating system: UNIX, Windows

*References are available upon request*