

MATT FRANCIS

515 S. DUNCAN AVE
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FAYETTEVILLE, ARKANSAS 72701
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OBJECTIVE

- Placement in a graduate program for advanced study in Electrical Engineering, in the area of Mixed-Signal IC design of Embedded Systems.

EDUCATION

1998 to present	University of Arkansas	Fayetteville, Arkansas
	<ul style="list-style-type: none">• Candidate for Bachelors of Science in Electrical Engineering, May 2003• Candidate for Bachelors of Science in Physics, Summer 2003• 141 Hours Completed, 3.85 GPA	

EXPERIENCE

- Knowledgeable with computer hardware and operating systems (Linux/Unix, Windows NT, etc) and programming languages (C, C++, Perl, Python, QT, Tcl/Tk).
- CAD drafting (AutoCAD) and EDA tool experience (Spice, Synopsis tools, Verilog HDL, Mentor Graphics, etc).

May 2000 to present	University of Arkansas Dept. of Electrical Engineering Mixed-Signal CAD Laboratory H. Alan Mantooth, Ph.D., P.E.	Fayetteville, Arkansas
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UNDERGRADUATE RESEARCHER/DESIGN LEAD

- Design lead: Group leader for user interfaces (GUI) for the lab's Paragon modeling software project. Responsible for design and development as well as directing the efforts of the GUI team, composed of more than four undergraduate and graduate research students.
- Research Assistant: Responsible for assisting with the design of tools to assist with CAD of Electronic Circuits for Sun Solaris 5.7. Utilizing skills in C, C++, TCL/TK, Make, and Unix shell scripting.

January to August 2001 May to August 2002	Hewlett-Packard Corp. Richardson VLSI Laboratory Tony Jones tonyj@rsn.hp.com	Richardson, Texas
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FUNCTIONAL DESIGN VERIFICATION ENGINEER (CO-OP)

- **May-August 2002**: Responsible for adapting the sdram memory model used for design verification of HP's next generation high-end server chipset to Double-Data-Rate II specification. This involved updating the c++ functional model, implementing the new topology in Verilog for simulation environments, and verifying erroneous operation of the chipset with designers.

- **January-August 2001:** Responsible for many aspects of the functional design verification of an ASIC to be used in high-performance, multi-processor computing systems, one of the largest ASIC's ever designed. Assisted with the design process by developing software memory models and writing directed tests used to verify the functionality of the chip. Utilized and gained experience in the areas of computer architecture, digital design (Verilog HDL), C++ and Perl.

**May 2000 to
December 2000**

**University of Arkansas
MBE/STM Facility
Paul Thibado, PhD**

Fayetteville, Arkansas

UNDERGRADUATE RESEARCH ASSISTANT

- Responsible for a research project involving use of internet development tools to create "virtual tours" of the MBE/STM facility as well as assisting graduate/doctoral students with their research.

**May 1999 to
August 1999**

**Bizjet International Sales and Support Tulsa, Oklahoma
3515 N. Sheridan Ave.
Tulsa, OK 74115**

CAD DRAFTSMAN/GRAPHIC DESIGNER

- Responsible for creating original drawings of custom cabinetry, avionics installations, and other structures necessary for FAA approval in corporate jet aircraft, design of paint schemes for jet aircraft and training and assisting fellow employees in CorelDraw and AutoCAD packages.

HONORS & PUBLICATIONS

- Awarded Chancellor's Scholarship to the University of Arkansas, full tuition and board, 1998-2002.
- Awarded Chevrolet Motors Scholarship, full tuition, spring 2003.
- Awarded Texas Instruments Undergraduate Research Scholarship.
- Awarded Eagle Scout Award, after 5 years of service in the Boy Scouts of America.
- Inducted into Golden Key, Tau Beta Pi (Engineering), and Eta Kappa Nu (Electrical Engineering) Honor Societies.

EXTRACURRICULAR ACTIVITIES & INTERESTS

- Section Leader, Drumline, University of Arkansas Marching Razorback Band, Hogwild Basketball Band.
- Percussionist/Section Leader, University of Arkansas Symphonic and Concert Bands.
- Vice-President, IEEE student chapter, member IEEE Computer Society.
- Vice-President, Eta Kappa Nu Electrical Engineering Honor Society student chapter.
- Program Chair, Tau Beta Pi Engineering Honor Society student chapter.

References are available upon request