

# XIAOLING HUANG

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## OBJECTIVE

- A research or engineering position in electronic design automation.

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## EDUCATION

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**August 1999 to present      University of Arkansas      Fayetteville, Arkansas**

**Ph.D. student in Electrical Engineering**

- Focused on modeling of mixed-signal circuits.
- Developed a technique for identifying nonlinear dynamical behavior.
- Implemented a tool for fully automatic behavioral model generation.
- GPA: 4.0

**Relevant Courses:**

Semiconductor Devices	Analog Integrated Circuits
Digital Control Systems	Stochastic DSP System Design
Telecommunication	Antennas & Radiation
Programming Windows GUI	Minicomputer Applications
Integrated Circuit Design	Mixed Signal Test
Modeling and Simulation of Mixed Signal Circuit	

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**Nov. 1997 to June 1999      National University of Singapore      Singapore**

**Master of Engineering in Electrical Engineering**

- Performed the design and simulation of dynamic voltage restorer (DVR)

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**Sept. 1993 to June 1996      Wuhan University      Wuhan, China**

**Master of Science in Electrical Engineering**

- Completed an expert system for distance protection of power systems.
- Developed database systems for power system protection.

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**Sept. 1989 to June 1993      Wuhan University      Wuhan, China**

**Bachelor of Science in Electrical Engineering**

- Senior-year project: programmed the communication between computers in the automatic control of power systems.

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## EXPERIENCE

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**August 1999  
to present      University of Arkansas      Fayetteville, Arkansas**  
**Dept. of Electrical Engineering**  
**H. Alan Mantooth, Ph.D., P.E.**

**Graduate Research Assistant**

- Working on the project "Algorithms and Tools for Automatic Behavioral Model Generation of Mixed-signal Systems-on-a-chip" sponsored by DARPA.

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**July 1996  
to Nov. 1997**

**Jilin Electric Power Corp.**

**Jilin, China**

**MIS Engineer**

- Participated in the development and maintenance of MIS.

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**COMPUTER SKILLS**

- Hardware description languages: VHDL-AMS, MAST, Verilog-A
- Programming languages: C++, FORTRAN, SQL
- Simulation tools: Pspice, Saber, Matlab,
- IC design tools: Mentor Graphics IC design tools
- Operating system: UNIX, Windows

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**PUBLICATIONS**

- **X. Huang**, C. Gathercole and H. A. Mantooh, "Modeling nonlinear dynamics in analog circuits via root localization," *IEEE Trans. on Computer-Aided Design*, accepted for publication, Jan. 2003.
- H. A. Mantooh, L. Ren, **X. Huang**, Y. Feng and W. Zheng, "A survey of bottom-up behavioral modeling methods for analog circuits", *Proceedings, IEEE International Symposium on Circuits and Systems*, May 25-28, 2003.
- V. Chaudhary, M. Francis, **X. Huang** and H. A. Mantooh, "Paragon – A mixed-signal behavioral modeling environment", *Proc. IEEE Intl. Conf. on Communications, Circuits and Systems*, vol. 2, pp. 1315 –1321, June 2002.
- **X. Huang** and H.A.Mantooh, "Event-driven electrothermal modeling of mixed-signal circuits", *Proceedings, 2000 IEEE/ACM International Workshop on Behavioral Modeling and Simulation*, pp.10~15, Oct. 2000.

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**EXTRACURRICULAR ACTIVITIES**

- IEEE student member

*References are available upon request*